

EXPEDITED PROCEDURE - EXAMINING GROUP 3729

S/N 10/628,594

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Robert C. Sundahl et al.

Examiner: Donghai Nguyen

Serial No.: 10/628,594

Group Art Unit: 3729

Filed: July 28, 2003

Docket No.: 884.264US2

Title: METHOD FOR MANUFACTURING AN INTERCONNECTED CIRCUIT  
BOARD ASSEMBLY AND SYSTEM

Assignee: Intel Corporation

Customer Number: 21186

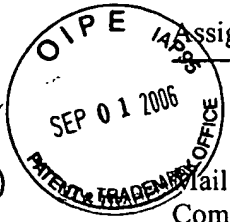
RESPONSE UNDER 37 C.F.R. 1.116

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In response to the Final Office Action mailed June 22, 2006, please amend the application as follows:

9/18/06

DN



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